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Date Submitted: August 11, 2004

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Application Number	09/680,517
Filing Date	10/6/2000
First Named Inventor	Hidehito KUBO
Group Art Unit	2126
Examiner Name	G. L. Opie
Attorney Docket Number	043034-0158

Sheet

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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS

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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
GO	A1		H10-240697	A	Hitachi LTD	09/11/1998		
GO	A2		H9-212467	A	Fujitsu LTD	08/15/1997		
GO	A3		H6-243112		Seuji Epson Corporation	09/02/1994		
GO	A4		H11-39340	A	NTT Data Tsushin KK	02/12/1999		
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GO	A6		H10-11407	A	Matsushita Electric Works, Ltd.	01/16/1998		
GO	A7		S57-757		Hitachi LTD	01/5/1982		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
GO	A8	Nariyoshi Yamai, "Design and Packaging of a Command Unit Load Distribution Function in UNIX," Journal of the Institute of Electronics, Information, and Communication Engineers, Vol. J77-D-I, No. 7, pp. 483 - 492, The Institute of Electronics, Information, and Communication Engineers, Ltd, 7/25/1994 (CS-NG-1998-01006-003)	
GO	A9	Susumu Tsuruda, "Dynamic Distributed Processing Systems Using Load Forecasting, and the Evaluation Thereof," Research Reports of the Institute of Electronics, Information, and Communication Engineers, Vol. 96, No. 95, pp. 25 - 32, COMP96-19, The Institute of Electronics Information, and Communications Engineers, Ltd., 6/14/1996 (CS-NG-1998-00585-004)	

Examiner
Signature

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Form PTO-1449 (MODIFIED)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 043034/0158		SERIAL NO. 09/680,517	
INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				APPLICANT Hidehito KUBO			
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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
FOREIGN PATENT DOCUMENTS							
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
GO	A1	10-312365	11-1998	Japan			Abstract
I	A2	10-27168	01-1998	Japan			Abstract
GU	A3	7-302242	11-1995	Japan			Abstract
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
GO	A4	Hisao KAMEDA <i>et al.</i> , "Optimal Load Balancing in Distributed Computer Systems", Springer, pp. 225-233, 1997.					
EXAMINER		George L. O'Pie			DATE CONSIDERED		
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